

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Akira IMAI, et al.

Application No.: TBD

Filed: July 22, 2003

For: MANUFACTURING METHOD OF SEMICONDUCTOR  
INTEGRATED CIRCUIT

Exp. Art Group: 1765

Exp. Examiner: William A. Powell

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR § 1.97 AND § 1.98**

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

July 22, 2003

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find a List, on a form substantially equivalent to Form PTO-1449, of documents cited in connection with a prior application of the above-identified application, Application No. 09/903,580, filed July 13, 2001.

Since Application No. 09/903,580 is being relied upon under 35 USC § 120 in the above-identified application, copies of the listed documents are not enclosed. See 37 CFR § 1.98(d).

This Information Disclosure Statement is being submitted concurrently with the filing of the above-identified application. Accordingly, requirements of 37 CFR § 1.97(b) are satisfied.

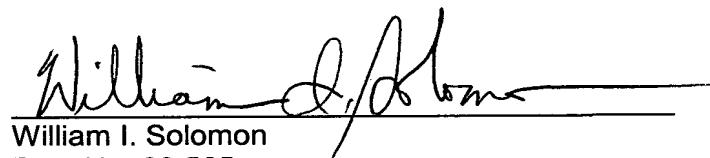
In view of the foregoing, it is respectfully submitted that all applicable requirements of 37 CFR § 1.97 and § 1.98 have been satisfied, in connection with

present submission of the enclosed list of documents. Accordingly, consideration of the listed documents, upon examination of the above-identified application, is respectfully requested.

To the extent necessary, Applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case No. 843.40352CX1), and please credit any excess fees to such Deposit Account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP



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William I. Solomon  
Reg. No. 28,565

WIS/sjg

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DKT. NO. 843.40352CX1	SERIAL NO. TBD
INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (Use several sheets if necessary)		APPLICANT A. Imai, et al.		
		FILING DATE July 22, 2003	EXP. GROUP 1765	

## U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date
	AA	5,750,441	05/98	Figura, et al.			
	AB	6,063,656	05/00	Clampitt			
	AC						

## FOREIGN PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Country	Class	Subclass	Translation/ Abstracts	
							Yes	No
	AD	5-19446	1/1993	Japan			X	
	AE	6-123963	5/1994	Japan			X	
	AF	6-289591	10/1994	Japan			X	
	AG	8-297359	11/1996	Japan			X	
	AG	11-84625	31999	Japan			X	

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AH			
AI			
Examiner		Date Considered	

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard St.3).

<sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC